



**Position: IC Design Engineer - Physical Design Verification**

**Location: Hong Kong / Shenzhen**

**Job Responsibilities:**

- Develop DRC/LVS/LPE/PERC deck and necessary script (experience in Calibre is an advantage);
- Responsible for QA flow to ensure the quality of the verification command file;
- Communicate with engineers to provide solutions on verification area;
- Learning for new verification tool and develop related deck;
- Performing layout of custom analog circuit blocks;
- Placing, routing and integrating digital standard cell blocks.

**Job Requirements:**

- Higher Diploma or Bachelor degree in Electronic Engineering or equivalent;
- 2 to 3 years of relevant working experience is preferable;
- Experience in managing foundry process/ design kit is an advantage;
- With knowledge in ESD / Latchup tools;
- With knowledge in semiconductor / fabrication process is an advantage
- Proficient in layout design and the associated software;
- Be proactive, willing to learn and passionate in physical design;
- Have good team spirit and sound communication skills;
- Fresh graduates will also be considered.

We offer competitive remuneration package and excellent career prospects to the right candidate. To apply, please send your full resume with current and expected salary by one of the following channels. Please quote our reference number in your application.



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SYSTECH**

1. Mail: Human Resources Department, Solomon Systech Limited, 6/F, No. 3 Science Park East Avenue, Hong Kong Science Park, Shatin, N.T., or
2. E-mail: [recruit@solomon-systech.com](mailto:recruit@solomon-systech.com), or
3. Upload the resume directly by clicking the apply button on each job opening

We regret that only short-listed candidates will be notified.